

IN THE UNITED STATES PATENT OFFICE

Applicants:	Eric D. Groen et al.		
Assignee:	Xilinx, Inc.		
Title:	MGT/FPGA CLOCK MANAGEMENT SYSTEM		
Serial No.:	10/660,254	File Date:	9/11/2003
Examiner:	Sophia Vlahos	Art Unit:	2611
Docket No.:	X-1359 US	Conf. No.:	5349

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OK TO ENTER: /S.V./

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Examiner:

In response to the Final Office Action mailed from the Patent Office on August 19, 2008, please replace/substitute the following claims as indicated.

**Amendment to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 8 of this paper.